

**AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph at page 3, lines 11-19, with the following amended paragraph:

The ~~bar-200~~ bars 202 are mutually separated by spaces 201 having a width  $W_e$ . The channel 230 is covered and in contact over a surface  $S_2$  (not shown in FIG. 2) with a gate 240. The extent of the surface  $S_2$  influences the value of the threshold voltage of the transistor. It is preferably as small as possible so as to limit the consumption of the transistor, but must be large enough to be capable of ensuring a good current level in the channel 230.